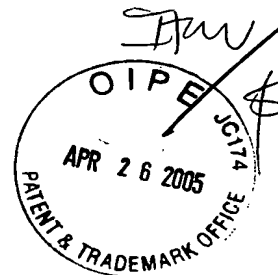


Docket No. 246239US2S



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Yoshinori KITAMURA, et al.

SERIAL NO: 10/728,928

GAU: 2814

FILED: December 8, 2003

EXAMINER: Theresa T. DOAN

FOR: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME

**INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97**

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

**REFERENCES**

- ☒ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☒ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

**RELATED CASES**

- ☐ Attached is a list of applicant's pending application(s), published application(s) or issued patent(s) which may be related to the present application. In accordance with the waiver of 37 CFR 1.98 dated September 21, 2004, copies of the cited pending applications are not provided. Cited published and/or issued patents, if any, are listed on the attached PTO form 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

**CERTIFICATION**

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

**DEPOSIT ACCOUNT**

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

  
Marvin J. Spivak

Registration No. 24,913

Joseph Scafetta, Jr.  
Registration No. 26,803

Eckhard H. Kuesters

Registration No. 28,870

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180.00 DP

Customer Number

**22850**

Tel. (703) 413-3000  
Fax. (703) 413-2220  
(OSMMN 05/03)

DOCKET NO.: 246239US2S



Page 1 of 1

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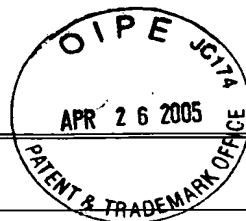
EXAMINER: Theresa T. DOAN

FOR: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE  
SAME

**STATEMENT OF RELEVANCY**

**Reference AO (JP 11-317464) on Form PTO-1449:**

This publication discloses a V-shaped groove formed in an STI region.

Form PTO 1449  
(Modified)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.  
246239US2SSERIAL NO.  
10/728,928

## LIST OF REFERENCES CITED BY APPLICANT

APPLICANT  
Yoshinori KITAMURA, et al.FILING DATE  
December 8, 2003GROUP  
2814

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	6,768,161 B2	07/27/2004	Hideyuki KINOSHITA			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO	11-317464	11/16/1999	JAPAN		X
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	Peter VanDerVoorn, et al., CMOS Shallow-Trench-Isolation to 50-nm Channel Widths", IEEE Transactions on Electron Devices, vol. 47, no. 6, June 2000, pages 1175-1182
	AX	
	AY	
	AZ	

☐ Additional References sheet(s) attached

Examiner

Date Considered

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.